

ABSTRACT OF THE DISCLOSURE

To design a chip having a plurality of circuit areas driven by different power supplies, a boundary cell to be  
5 inserted on the boundary between the circuit areas is prepared. After creating a logic circuit netlist with a design tool, the boundary cell is inserted on the boundary. The boundary cell is connected on a signal transmission path between the circuit areas. A circuit for suppressing  
10 short-through current or leakage current is used as the boundary circuit. By preparing the boundary cell in a cell library, chip design is facilitated.